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§ Group/Art Unit: 2123
§ Atty. Dkt. No: 5150-23006
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Name of Registered Representative

5/22/2003
Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This paper is submitted in response to the Office Action of March 25, 2003, to further highlight why the application is in condition for allowance.

IN THE CLAIMS:

23. (Amended) The system of claim 22, wherein the structure node is one of an iteration node or a looping node;

wherein the structure node includes at least one of a period parameter and a phase delay parameter, wherein the period parameter indicates a period of execution for cycles of the structure node, and wherein the phase delay parameter indicates a phase delay of cycles of the structure node.

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